Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	112	((transmit\$4 driver (driving adj circuit) (output adj buffer)) same ((current adj driver) (constant adj current)) same (control (impedance adj circuit)) same modes) and ("326"/\$.ccls. "327"/\$.ccls.)	US-PGPUB; USPAT	OR	ON	2008/01/03 14:12
L2	31	((transmit\$4 driver (driving adj circuit) (output adj buffer)) same ((current adj driver) (constant adj current)) same (control (impedance adj circuit)) same (modes ((high low) adj speed))) and ("326"/\$.ccls."327"/\$.ccls.) not L1	US-PGPUB; USPAT	OR	ON	2008/01/03 14:10
L3	115	((transmit\$4 driver (driving adj circuit) (output adj buffer)) same ((current adj driver) (constant adj current)) same (control (impedance adj circuit)) same (modes ((high low) adj (speed frequency)))) and ("326"/\$.ccls. "327"/\$.ccls.) not L2	US-PGPUB; USPAT	OR	ON	2008/01/03 14:10
L4	. 12	high adj speed and low adj speed and data and current adj driver and buffer and select\$3 and impedance with control\$3 and switch	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/03 14:12
L5	112	((transmit\$4 driver (driving adj circuit) (output adj buffer)) same ((current adj driver) (constant adj current)) same (control (impedance adj circuit)) same modes) and ("326"/\$.ccls. "327"/\$.ccls.)	US-PGPUB; USPAT	OR	ON	2008/01/03 14:12
L6		((transmit\$4 driver (driving adj circuit) (output adj buffer)) same ((volt\$3 adj driver) (constant adj volt\$3)) same (control (impedance adj circuit)) same (modes ((high low) adj speed))) and ("326"/\$.ccls. "327"/\$.ccls.)	US-PGPUB; USPAT	OR	ON .	2008/01/03 14:10
L7	31	((transmit\$4 driver (driving adj circuit) (output adj buffer)) same ((current adj driver) (constant adj current)) same (control (impedance adj circuit)) same (modes ((high low) adj speed))) and ("326"/\$.ccls. "327"/\$.ccls.) not L1	US-PGPUB; USPAT	OR	ON	2008/01/03 14:10

			,			
L8	40	((transmit\$4 driver (driving adj circuit) (output adj buffer)) same ((current adj driver) (constant adj current)) same (control (impedance adj circuit)) same modes).clm.	US-PGPUB; USPAT	OR	ON	2008/01/03 14:13
L9	0	(high adj speed and low adj speed and data and current adj driver and buffer and select\$3 and impedance with control\$3 and switch).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/03 14:12
L10	40	((transmit\$4 driver (driving adj circuit)) same ((current adj driver) (constant adj current)) same (control (impedance adj circuit)) same modes).clm.	US-PGPUB; USPAT	OR ·	ON	2008/01/03 14:13
L11	115	((transmit\$4 driver (driving adj circuit)) same ((current adj driver) (constant adj current)) same modes).clm.	US-PGPUB; USPAT	OR	ON	2008/01/03 14:13
L12	2097	(326/83).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/03 14:17
L13	1930	(326/30).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/03 14:17
L14	2408	(327/108).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/03 14:17
L15	767	(L12 L13 L14) and (impedance same control) and buffer	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON ·	2008/01/03 14:17

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S1	4	(("6075384") or ("6275066")).PN.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/24 09:36
S2	5	("5587709" "6002268" "6075384").PN. OR ("6275066"). URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/09/24 09:44
S3	10	("5587709" "5604450").PN. OR ("6075384").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/09/24 09:56
S4	6	("5767698" "6072747" "6075384" "6326802" "6330193" "6332661").PN. OR ("6876225"). URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/09/24 10:35
S5	3	("5767698" "6075384" "6332661").PN. OR ("6525570"). URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/09/24 10:39
S6	0	(high adj speed and low adj speed and data and current adj driver and buffer and select\$3 and impedance with control\$3 and switch).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/24 10:44
S7	0	(((high adj speed) (low adj speed)) and data and current adj driver and buffer and select\$3 and impedance with control\$3 and switch).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/24 10:42
S8	0	(speed and data and current adj driver and buffer and select\$3 and impedance with control\$3 and switch).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR ·	ON	2007/09/24 10:42
S9	2	(data and current adj driver and buffer and select\$3 and impedance with control\$3 and switch).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/24 10:44

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S10	12	high adj speed and low adj speed and data and current adj driver and buffer and select\$3 and impedance with control\$3 and switch	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/24 13:09
S11	20895	(transmit\$4 driver (driving adj circuit) (output adj buffer)) and ((current adj driver) (constant adj current)) and (control (impedance adj circuit)) and modes	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/24 13:25
S12	881	(transmit\$4 driver (driving adj circuit) (output adj buffer)) same ((current adj driver) (constant adj current)) same (control (impedance adj circuit)) same modes	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/24 13:26
S13	611	(transmit\$4 driver (driving adj circuit) (output adj buffer)) same ((current adj driver) (constant adj current)) same (control (impedance adj circuit)) same modes	US-PGPUB; USPAT	OR	ON	2007/09/24 13:26
S14	72675	((transmit\$4 driver (driving adj circuit) (output adj buffer)) same ((current adj driver) (constant adj current)) same (control (impedance adj circuit)) same modes) and "326"/\$.ccls. "327"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/09/24 13:27
S15	- 111	((transmit\$4 driver (driving adj circuit) (output adj buffer)) same ((current adj driver) (constant adj current)) same (control (impedance adj circuit)) same modes) and ("326"/\$.ccls. "327"/\$.ccls.)	US-PGPUB; USPAT	OR ·	ON	2007/09/24 13:42
S16	431	((transmit\$4 driver (driving adj circuit) (output adj buffer)) same ((volt\$3 adj driver) (constant adj volt\$3)) same (control (impedance adj circuit)) same modes)	US-PGPUB; USPAT	OR	ŐΝ	2007/09/24 13:36
S17	81	((transmit\$4 driver (driving adj circuit) (output adj buffer)) same ((volt\$3 adj driver) (constant adj volt\$3)) same (control (impedance adj circuit)) same (modes ((high low) adj speed))) and ("326"/\$.ccls. "327"/\$.ccls.)	US-PGPUB; USPAT	OR	ON	2007/09/24 13:49

S18	30	((transmit\$4 driver (driving adj circuit) (output adj buffer)) same ((current adj driver) (constant adj current)) same (control (impedance adj circuit)) same (modes ((high low) adj speed))) and ("326"/\$.ccls."327"/\$.ccls.) not S15	US-PGPUB; USPAT	OR	ON	2007/09/24 13:49
S19	114	((transmit\$4 driver (driving adj circuit) (output adj buffer)) same ((current adj driver) (constant adj current)) same (control (impedance adj circuit)) same (modes ((high low) adj (speed frequency)))) and ("326"/\$.ccls. "327"/\$.ccls.) not S18	US-PGPUB; USPAT	OR	ON	2007/09/24 13:47
S20	84	((transmit\$4 driver (driving adj circuit) (output adj buffer)) same ((volt\$3 adj driver) (constant adj volt\$3)) same (control (impedance adj circuit)) same (modes ((high low) adj (speed frequency)))) and ("326"/\$.ccls."	US-PGPUB; USPAT	OR	ON	2007/09/24 13:49
S21	2039	(326/83).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/10/01 09:14
S22	1878	(326/30).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/10/01 09:14
S23	2367	(327/108).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/10/01 09:15
S24	1458	(S21 S22 S23) and impedance same control	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/01 09:15

S25	745	(S21 S22 S23) and (impedance same control) and buffer	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/01 09:16
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